<u>Digital Signatures for</u> <u>Flows and Multicasts</u>

by Chung Kei Wong and Simon S. Lam in IEEE/ACM Transactions on Networking, August 1999

Digital Signatures (Simon S. Lam) 1

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Digital Signature

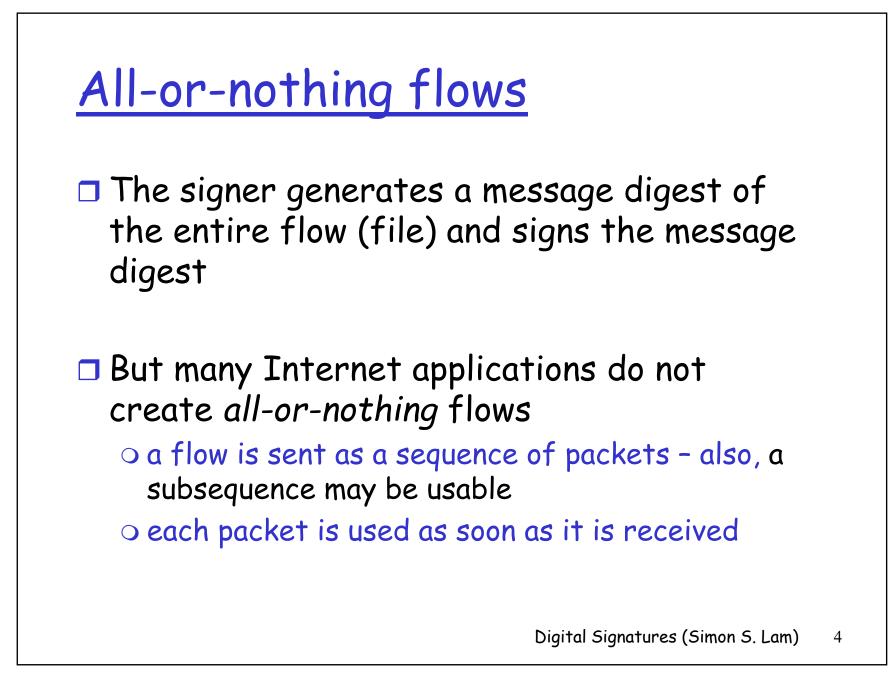
- Examples: RSA, DSA
- Provide authenticity, integrity and nonrepudiation
- How to sign and verify?
 signing key k_s, verification key k_v, message digest h(m)
 signature = sign(h(m), k_s)

 \circ verify(signature, h(m), k_v) = True/False

Signing & verification operations are slow compared to symmetric key operations

<u>Motivation</u>

Traditional network applications (circa 1998) message-oriented unicast, e.g., email, file transfer, client-server Emerging network applications flow-oriented, e.g., audio, video, stock quotes multicast, e.g., teleconference, software distribution Problem: How to sign/verify efficiently for high-speed transmissions? o real-time generated flows delay-sensitive packet flows

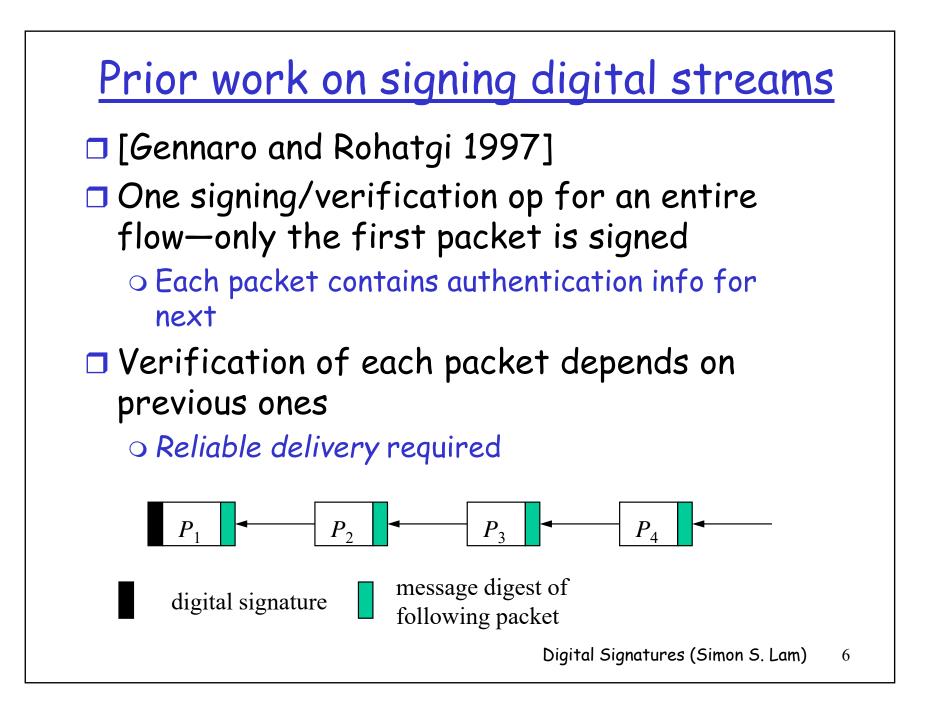


Sign-each Approach

- □ A flow is a sequence of data packets
- Sign each packet individually
- Inefficient: one signing/verification operation per packet
- Rates on a Pentium-II 300 MHz using 100% processing time (with 512-bit modulus)

Packet	Rate (packets/sec)					
size	Sigi	ning	Verification			
(bytes)	RSA	DSA	RSA	DSA		
512	78.8	176	2180	128		
1024	78.7	175	1960	127		

Update: today's processor speed is much higher but Cisco's recommended RSA modulus size is 2048 bits to 4096 bits

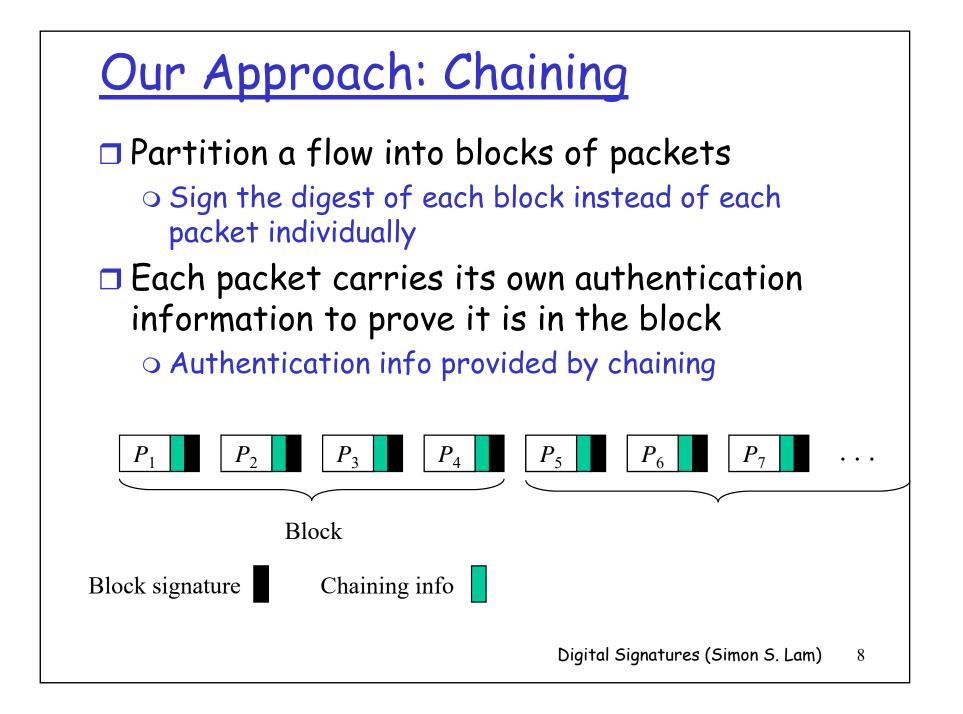


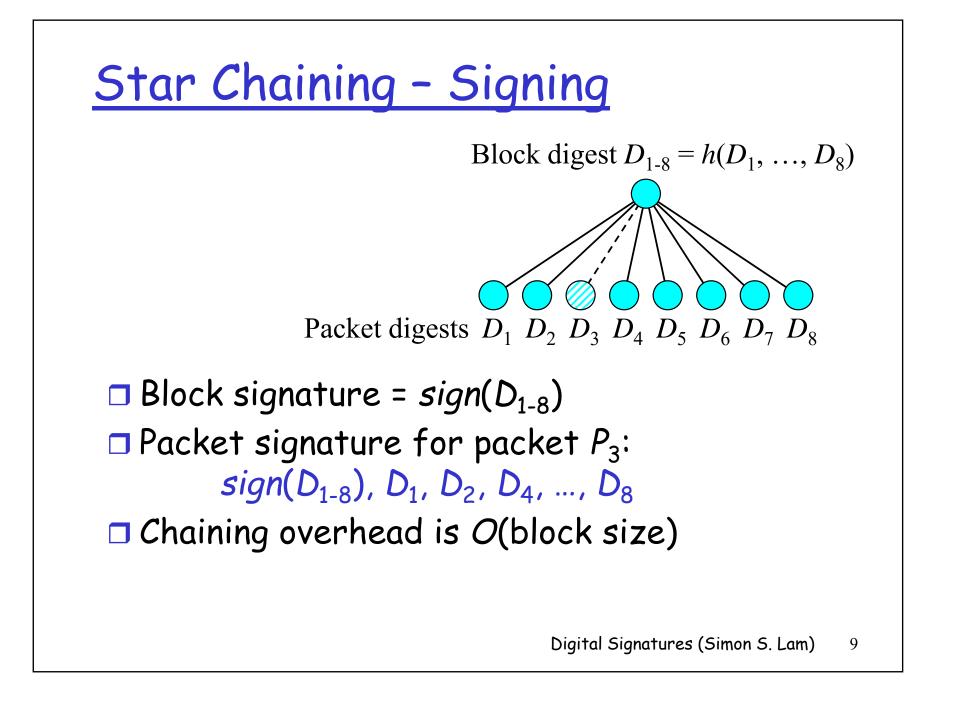
Flow Signing Problem

- Each packet may be used as soon as it is received
- Subsequences of a flow are received and used
 best-effort delivery, e.g., UDP, IP multicast
 different needs/capabilities, e.g., layered video

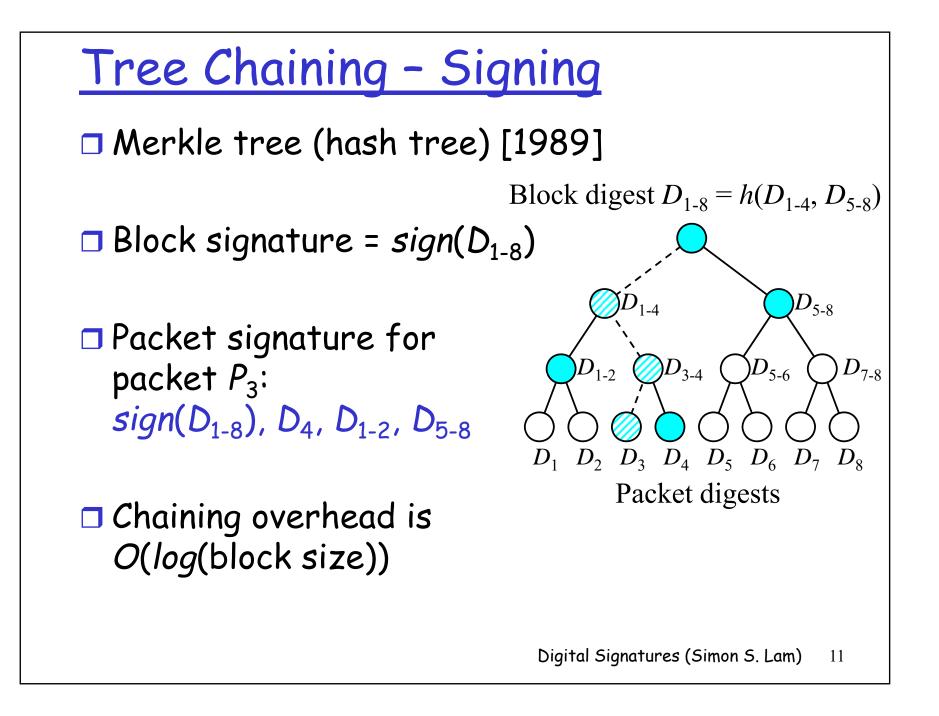
How to efficiently sign flows with each packet being individually verifiable?

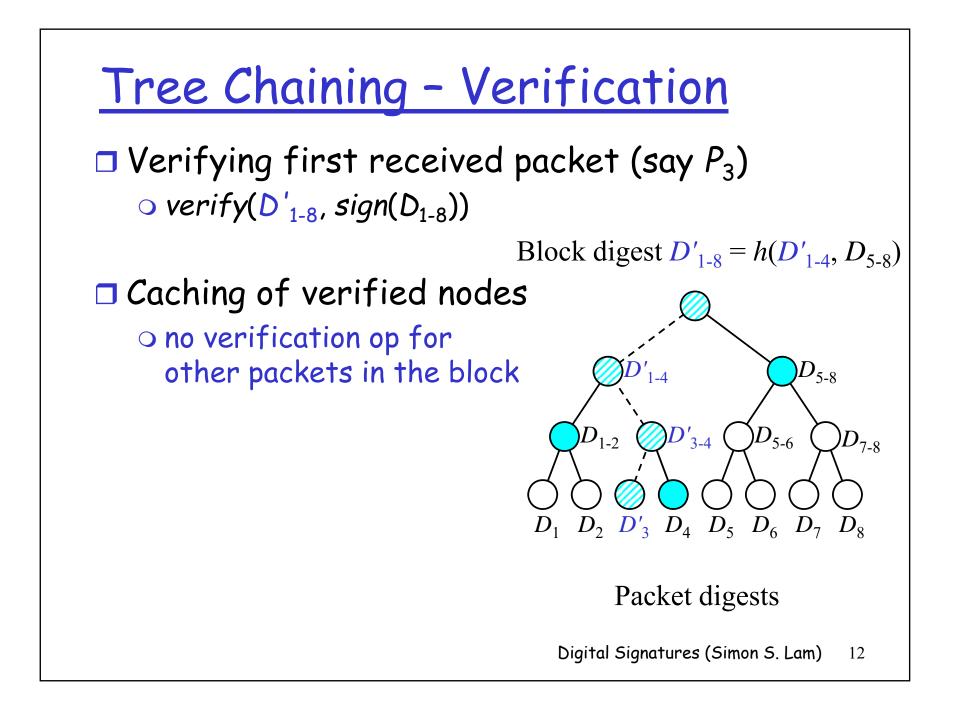
 Actually, packets do not have to belong to the same flow to reduce signing cost! E.g. in a multicast

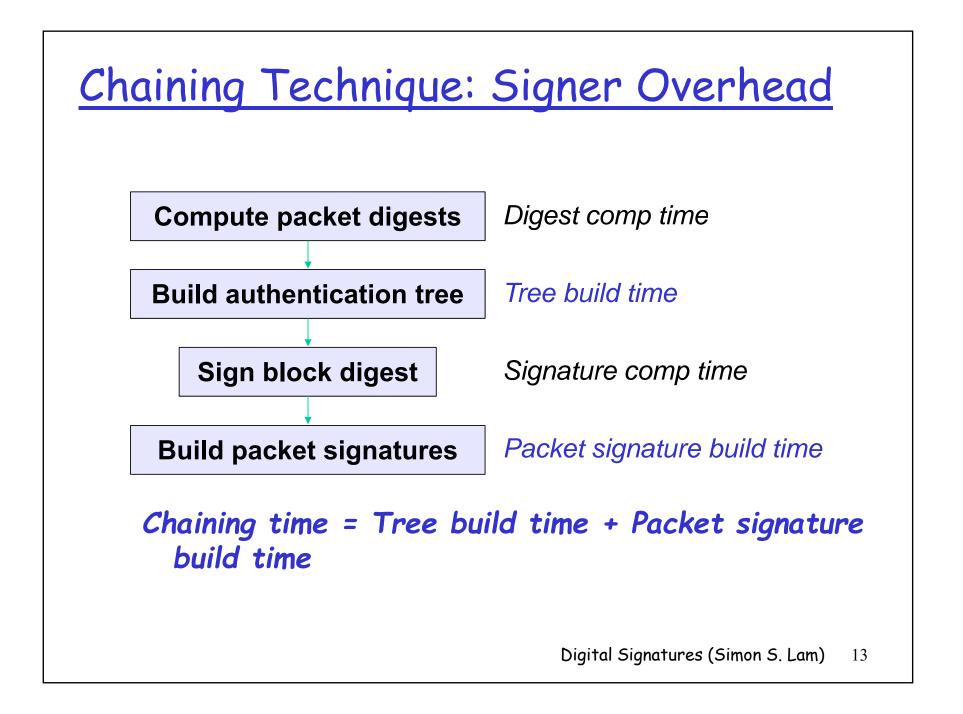


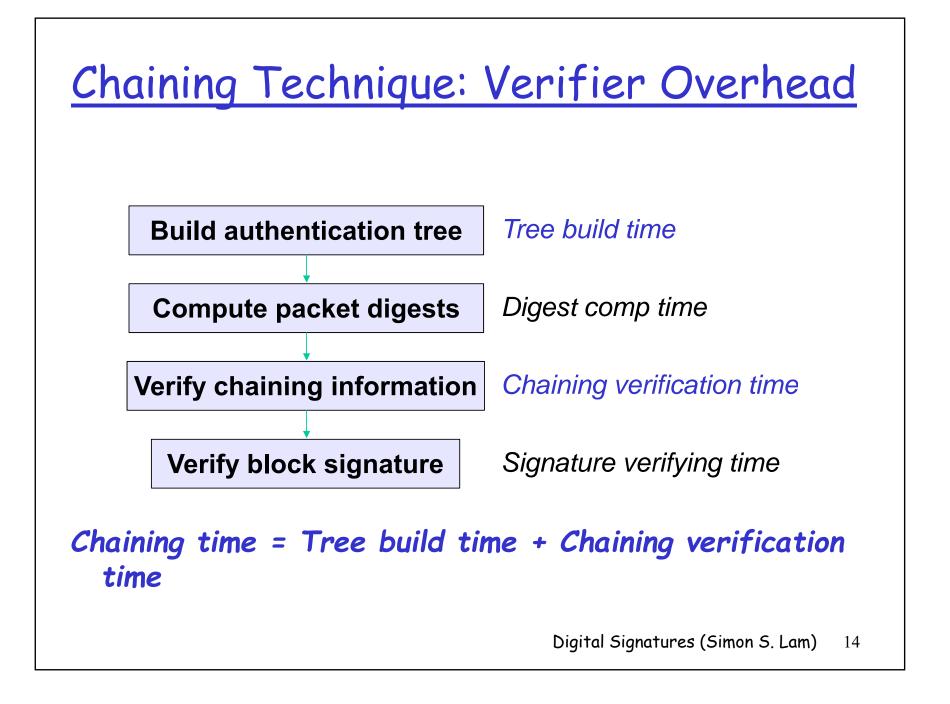


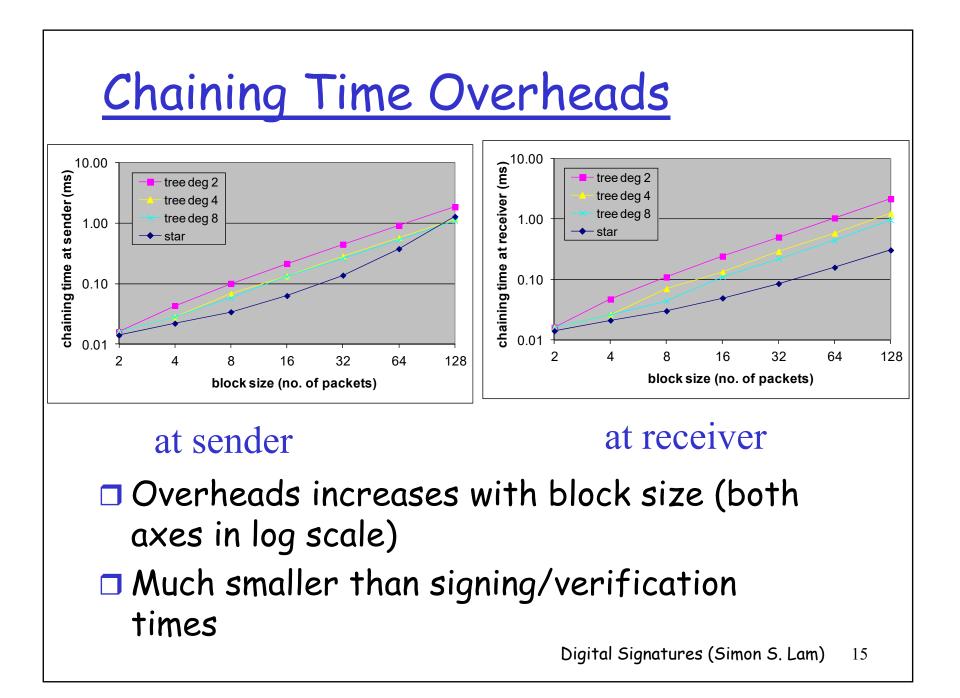
Star Chaining - Verification \Box Verifying first received packet (say P_3) Block digest $D'_{1-8} = h(D_1, D_2, D'_3, D_4, ..., D_8)$ $D_1 D_2 D'_3 D_4 D_5 D_6 D_7 D_8$ \bigcirc verify(D'_{1-8} , sign(D_{1-8})) Packet digests Caching of verified nodes o no verification op for other packets in the block Digital Signatures (Simon S. Lam) 10

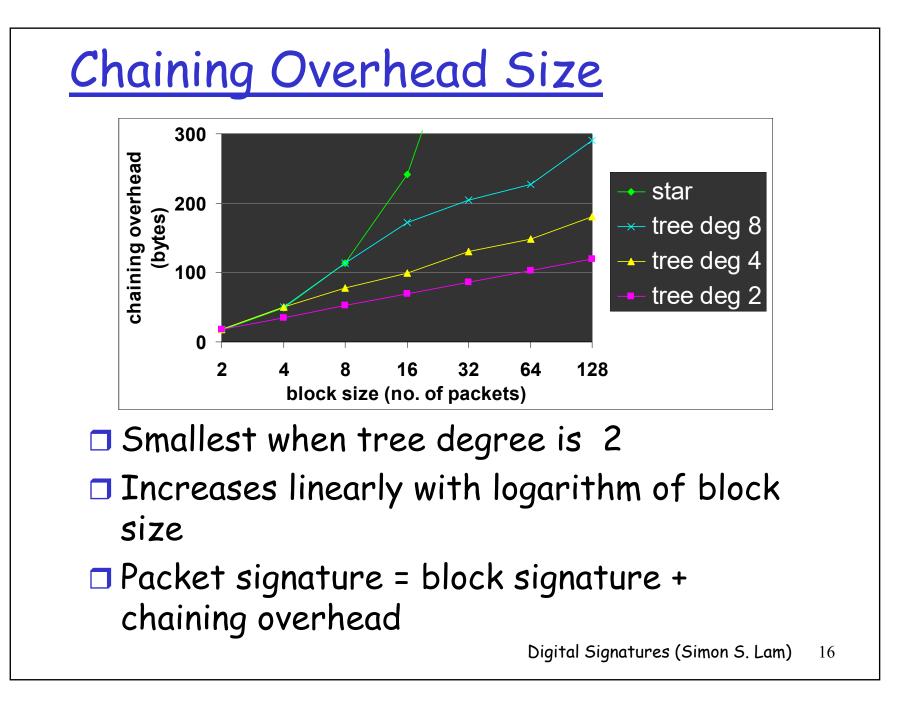


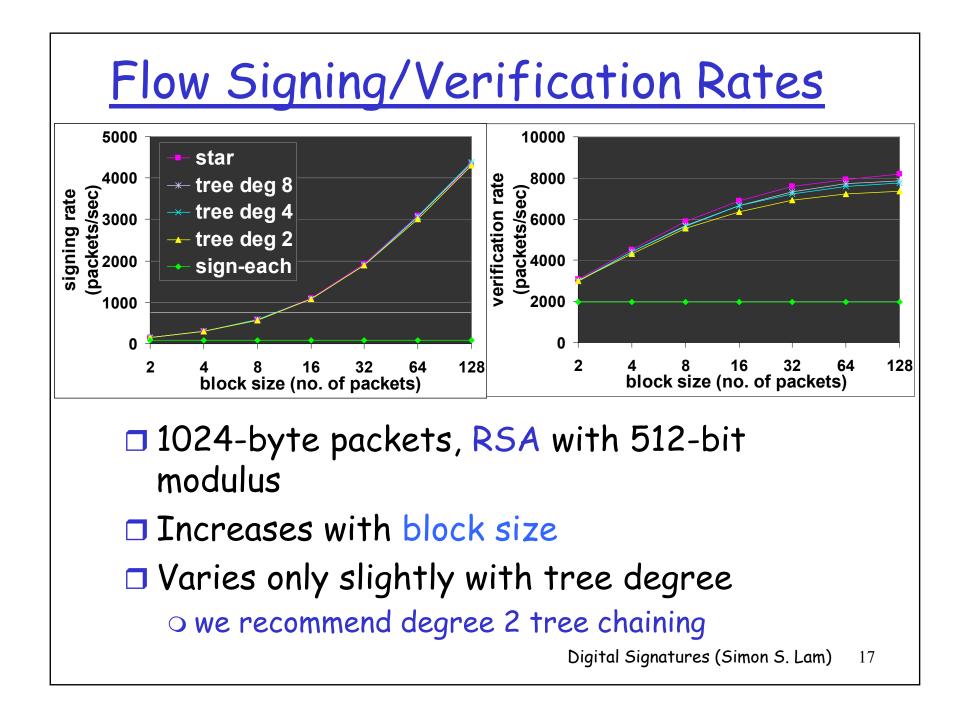








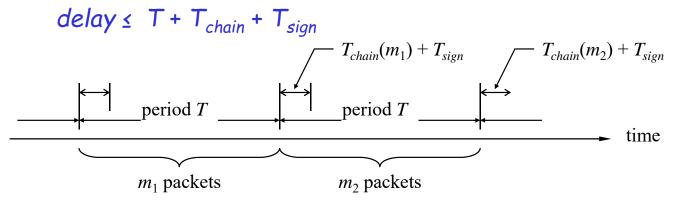




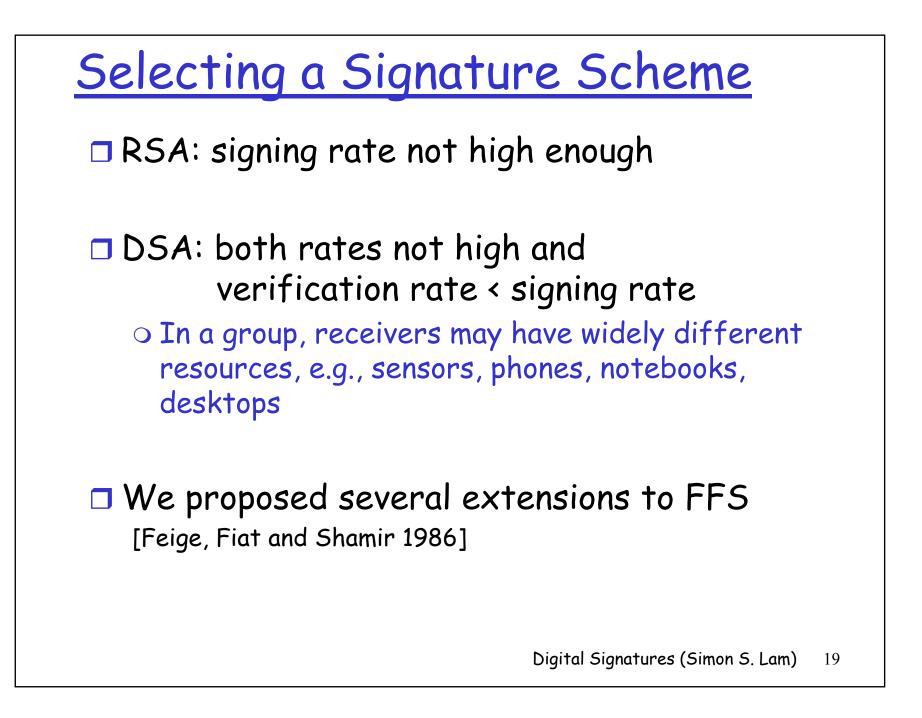
Real-time Generated Flows

- Fixed block size for non-real-time generated flows
- Fixed time period T for real-time generated flows

• Bounded delay signing since for any packet:



• T should be larger than $T_{chain} + T_{sign}$ • delay cannot be smaller than $2(T_{chain} + T_{sign})$



FFS Signer

choose two large primes p and q
compute modulus n = pq
choose integers v₁, ..., v_k s₁, ..., s_k such that s_i² = v_i⁻¹ mod n
signing key is {s₁, ..., s_k, n}
verification key is {v₁, ..., v_k, n}

How to Sign Message m

choose t random integers, r₁, ..., r_t, between 1 and n

 $\Box \text{ compute } x_i = r_i^2 \mod n, \text{ for } i = 1, ..., t$

compute digest
$$h(m, x_1, ..., x_t)$$
 of message m

where function $h(\cdot)$ is public knowledge and produces a digest of at least $k \times t$ bits

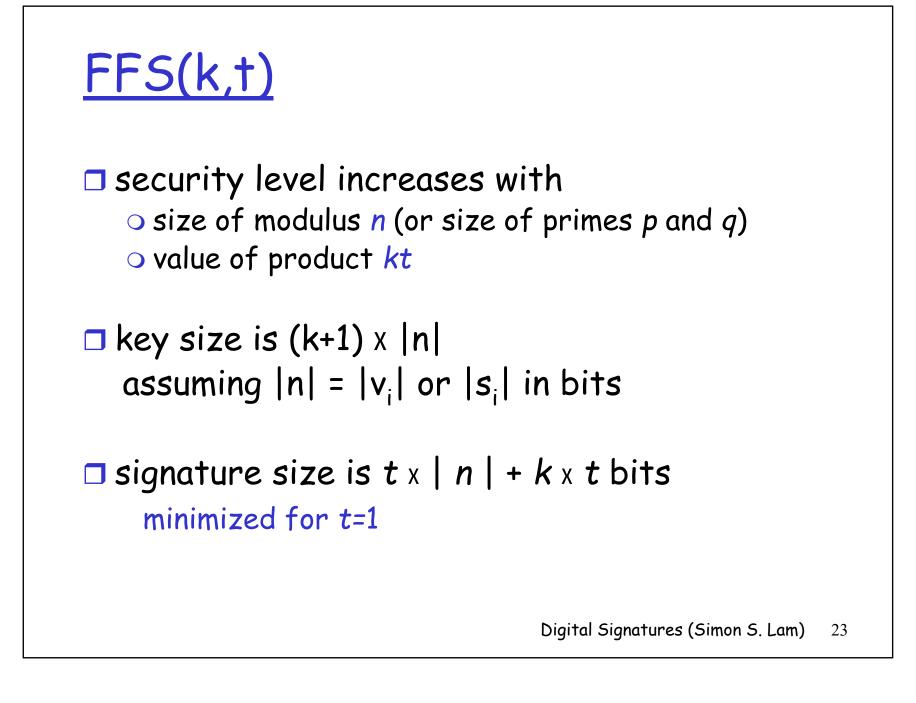
let
$$\{b_{ij}\}$$
 be the first $k \times t$ bits of the digest

 $\Box \text{ compute } y_i = r_i \times (s_1^{b_{i1}} \times ... \times s_k^{b_{ik}}) \mod n$

signature of m consists of $\{y_i\}$ and $\{b_{ii}\}$ for i = 1, ..., t and j = 1, ..., k

How to Verify Signature of Message m

□ signature of m $\{y_i\}$ and $\{b_{ij}\}$ for i = 1, ..., t and j = 1, ..., k \Box compute $z_i = y_i^2 \times (v_1^{b_{i1}} \times \dots \times v_k^{b_{ik}}) \mod n$ for *i* = 1, ..., *t* it can be shown that z_i is equal to x_i at the signer □ signature is valid if and only if the first $k \times t$ bits of $h(m, z_1, ..., z_t)$ are equal to the $\{b_{ii}\}$ received in signature



FFS key and signature sizes

FFS Signing/Verification Key and Signature Sizes (Bytes) With 512-Bit Modulus

	t = 1		t=2		t = 4	
	key	sig	key	sig	key	sig
kt = 64						
kt = 128	8256	80	4160	144	2112	272

For a fixed kt product, signature size is minimized for t =1, but key size is maximized

eFFS Signature Scheme

Several extensions to FFS [Feige, Fiat and Shamir 1986]

• Faster signing

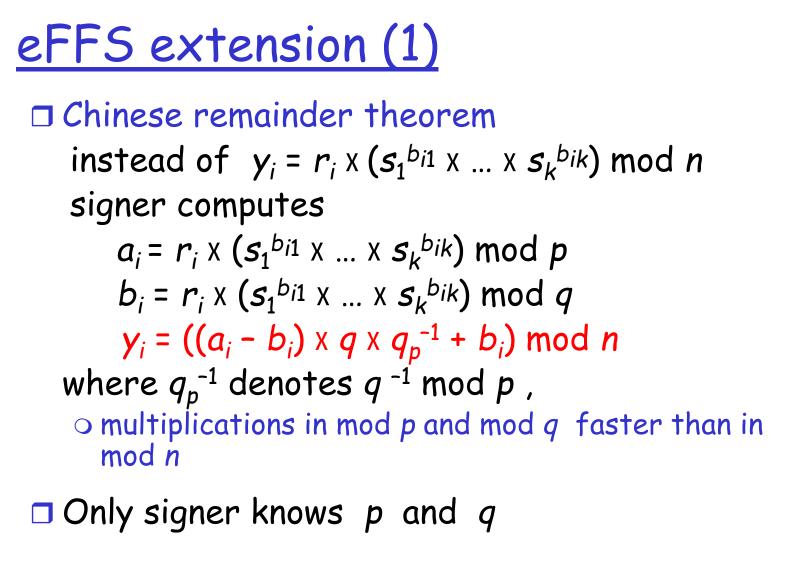
- Chinese remainder theorem (crt)
- Precomputation (4-bit, 8-bit)

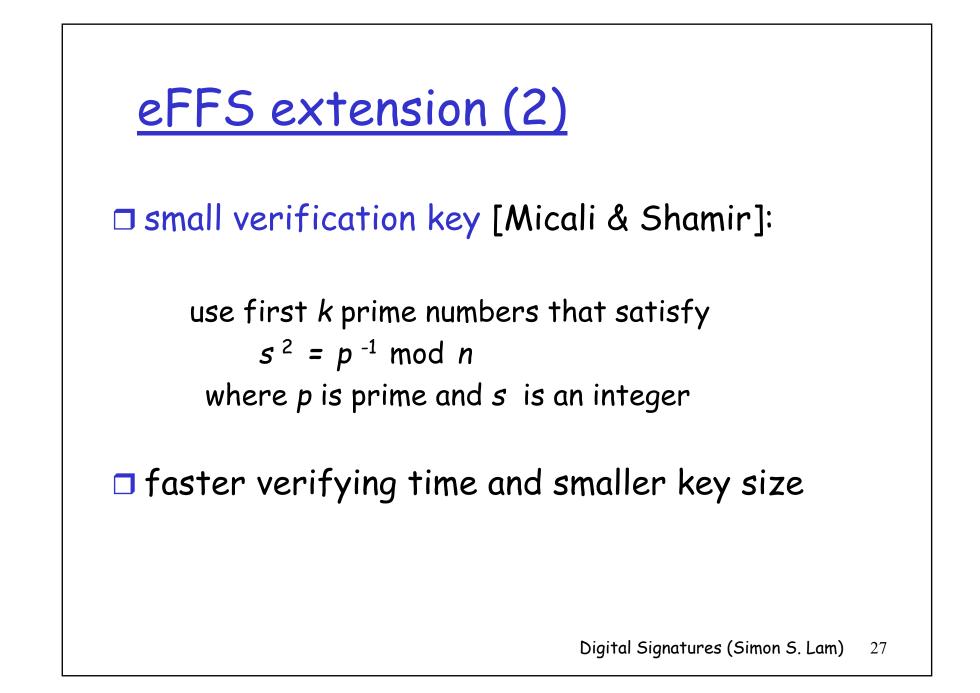
Faster verification

• Small verification key (*sv-key*) [Micali & Shamir 1990]

Adjustable and incremental verification

- multilevel signature
- lower security level with less processor time at receiver
- security level can be increased later by more processor time





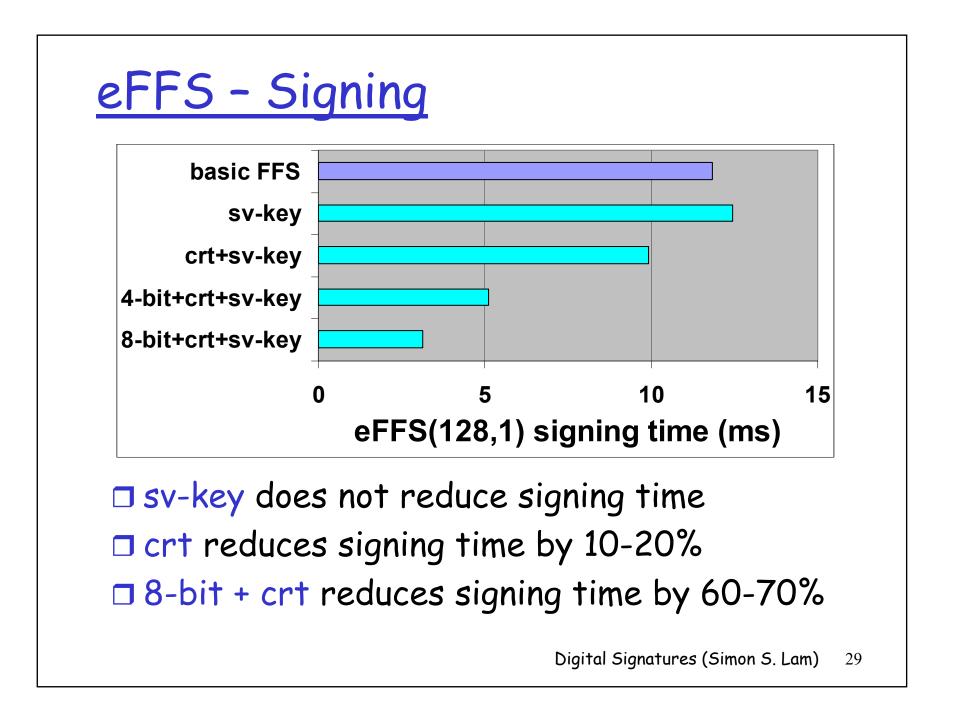
eFFS extension (3)
To compute
$$y_i = r_i \times (s_1^{b_{i1}} \times ... \times s_k^{b_{ik}}) \mod n$$

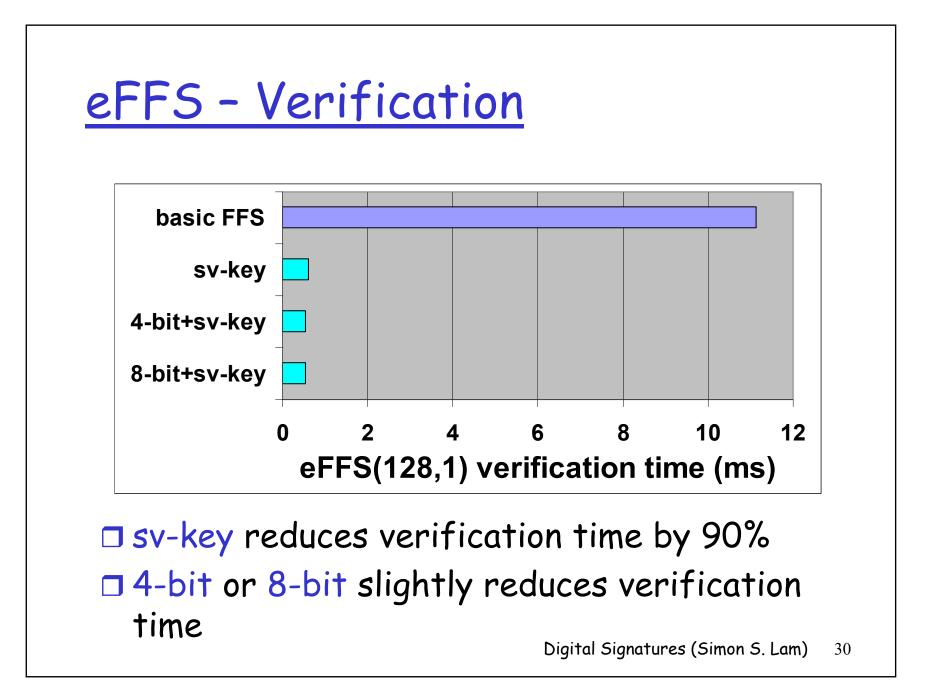
for $i = 1, ..., t$

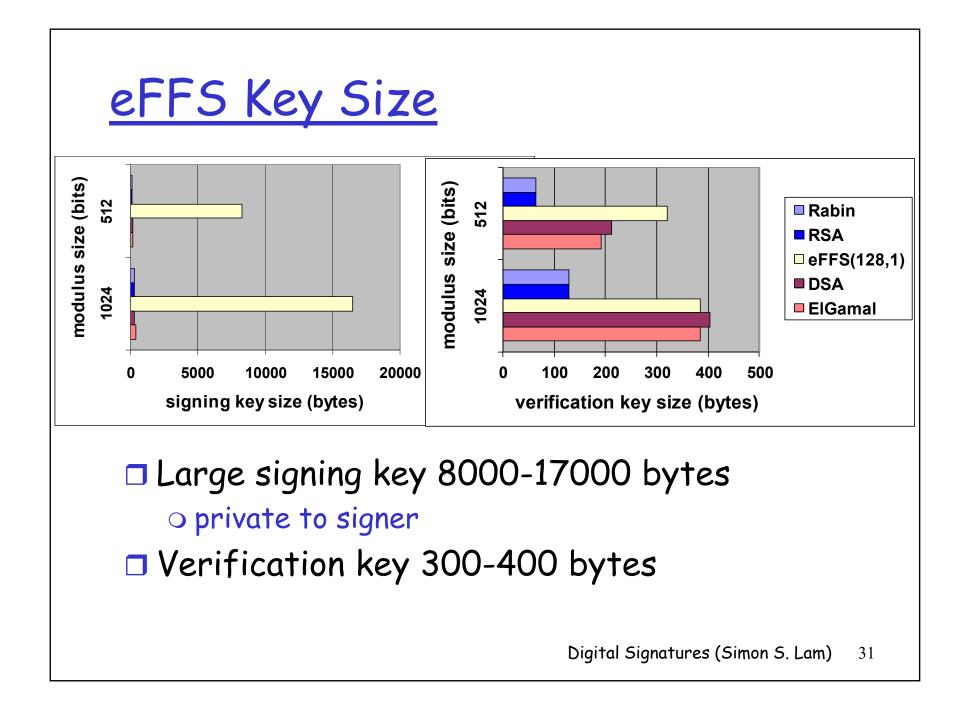
 $\Box \text{ precomputation of } (s_1^{b_{i1}} \times ... \times s_k^{b_{ik}})$

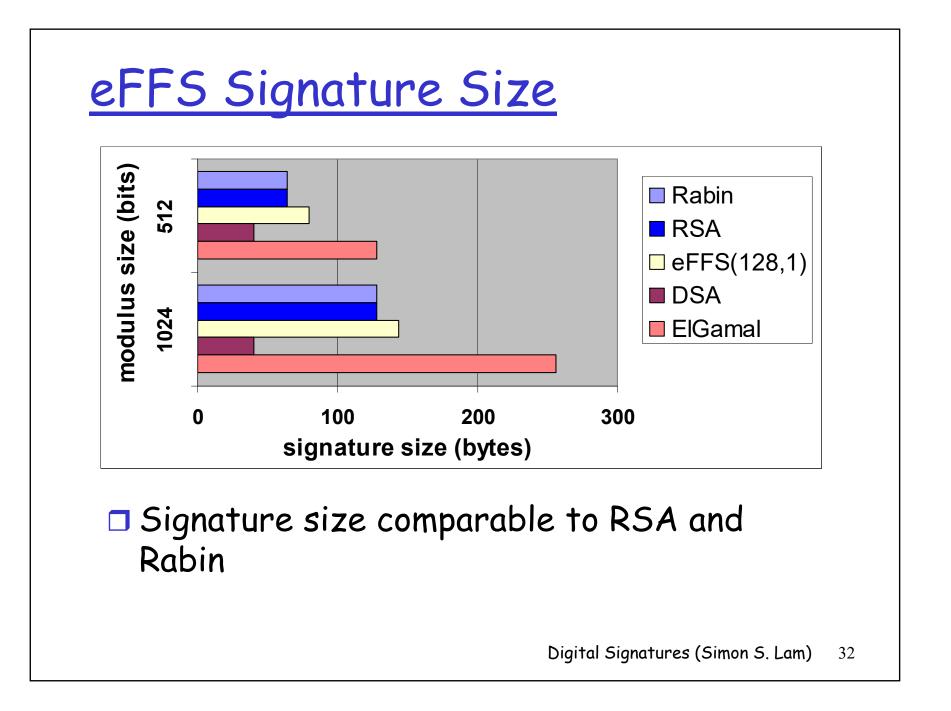
additional memory of 31 KB and 261 KB required for 4-bit and 8-bit precomp respectively

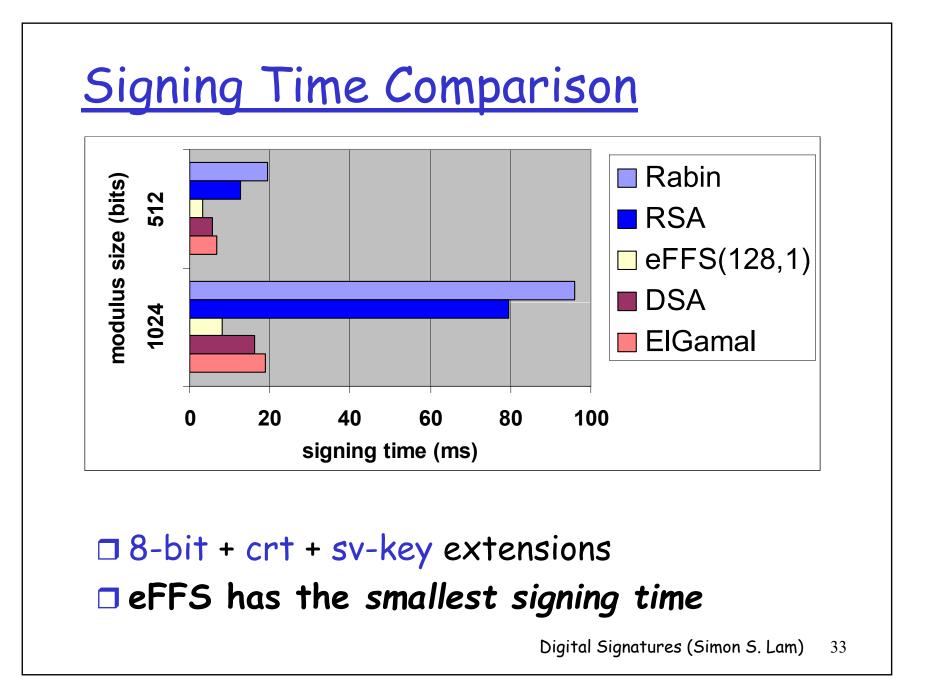
 only minor improvement at verifier when used with small v-keys

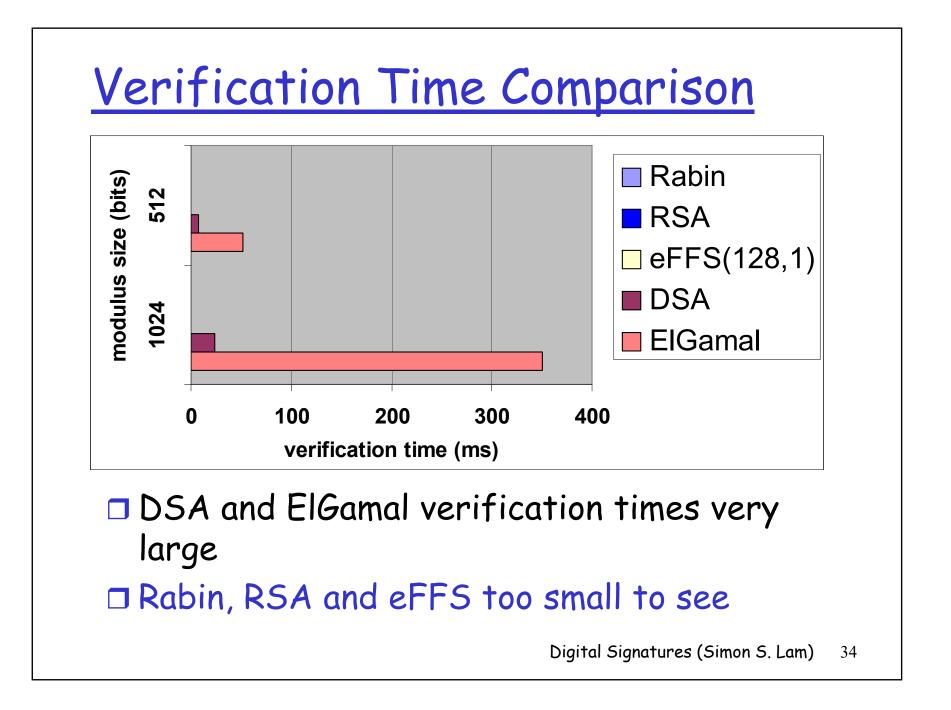


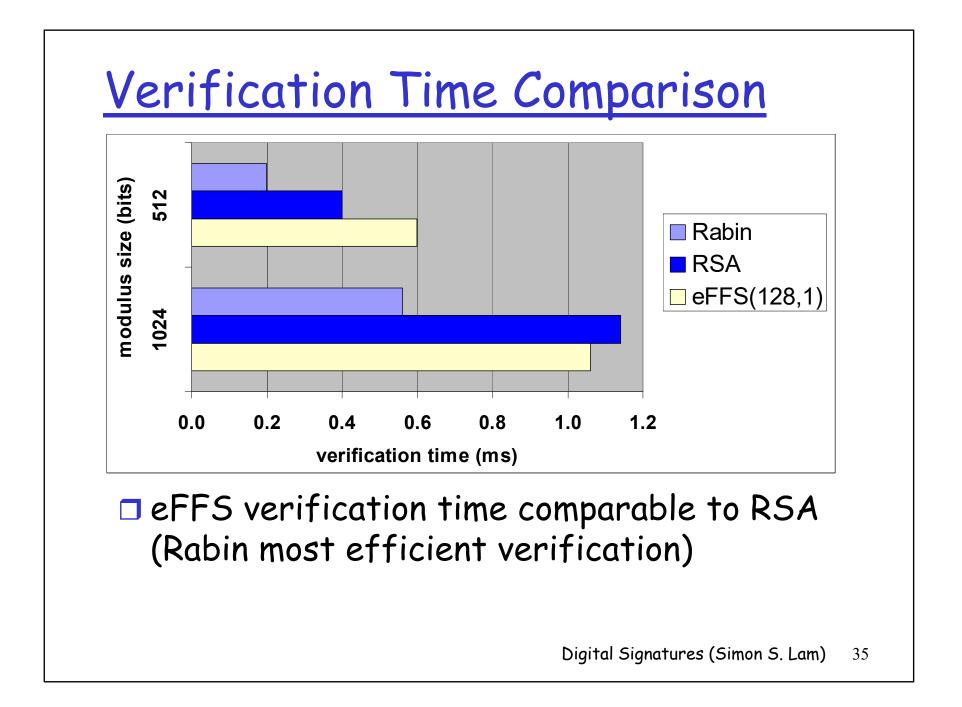


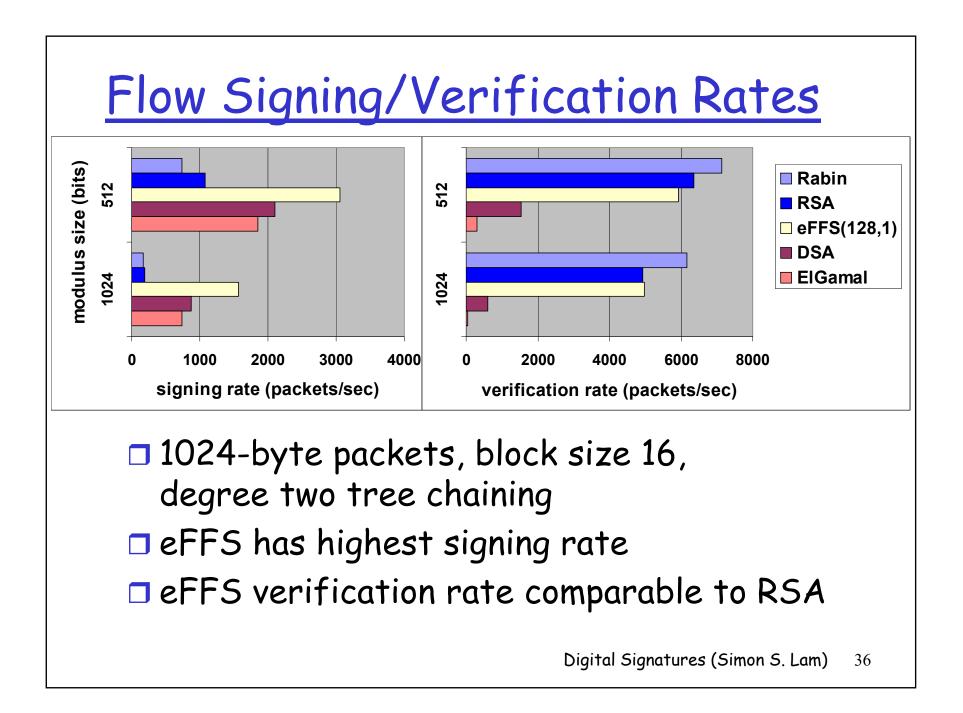












eFFS Adjustable and **Incremental Verification** \Box Security level of eFFS(k,t) depends on modulus size and product kt o same kt and modulus size ~ same security level Adjustable and incremental verification \bigcirc using t > 1 with additional info in signature ○ up to t steps o adjustable and incremental: receiver verifies steps one by one

eFFS Adjustable and Incremental Verification (cont.) \Box t-level signature includes { x_i } for i = 2, ..., tnote that $\{x_i\}$ can be computed from original signature together with verification key \Box verify a t-level signature at security level $l \leq t$, (1) compute $z_i = y_i^2 \times (v_1^{b_{i1}} \times ... \times v_k^{b_{ik}}) \mod n$ for i = 1, ..., I, (2) verify that the first $k \times t$ bits of $h(m, z_1, x_2, ..., x_t)$ are equal to the $\{b_{ii}\}$ received and z_2, \dots, z_l are equal to x_2, \dots, x_l

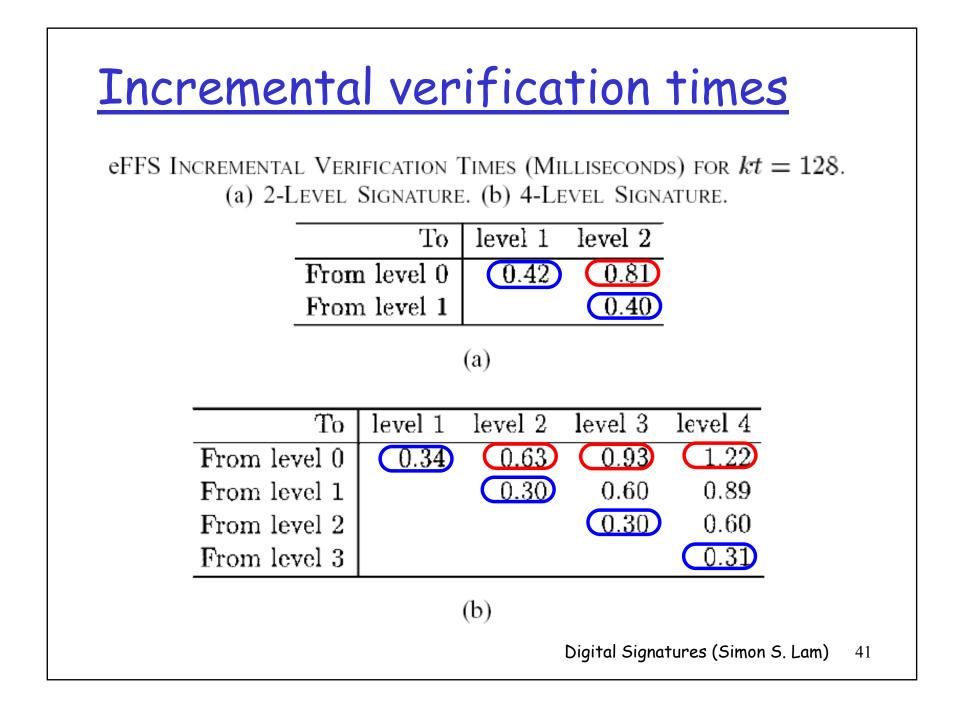
eFFS Adjustable and Incremental Verification (cont.) \Box increase security level from I_1 to I_2 , (1) compute $z_i = y_i^2 \times (v_1^{b_{i1}} \times ... \times v_k^{b_{ik}}) \mod n$ for $i = l_1 + 1, \dots, l_2$ (2) verify that $z_{l_{1}+1}, ..., z_{l_{2}}$ are equal to $x_{l_{1}+1}, ..., x_{l_{2}}$ Digital Signatures (Simon S. Lam) 39

Incremental signing times

eFFS t-Level Signature Signing Times (Milliseconds)

	kt produci			
	kt = 32	kt = 64	kt = 128	
1-level signature	1.47	2.02	3.14	
2-level signature		2.87	3.98	
4-level signature			5.67	

2-level signature takes less time to sign than two 1-level signatures



Conclusions

Flow signing/verification procedures

- much more efficient than sign-each
- \odot small communication overhead
- can be used by a sender that signs a large number of packets to different receivers
 - there is no requirement that the packets belong to a flow but if they do, verification is also more efficient; else, each receiver has to do a bit more work

eFFS digital signature scheme

 most efficient signing compared to RSA, Rabin, DSA, and ElGamal

 highly efficient verification and comparable to RSA (only Rabin is more efficient)

o adjustable and incremental verification

